

Form C: Type Test Verification Report

Type Approval and Manufacturer declaration of compliance with the requirements of G98.

This form should be used when making a Type Test submission to the Energy Networks Association (ENA).

If the **Micro-generator** is **Fully Type Tested** and already registered with the ENA **Type Test Verification Report** Register, the **Installation Document** should include the **Manufacturer**'s Reference Number (the Product ID), and this form does not need to be submitted.

Where the **Micro-generator** is not registered with the ENA **Type Test Verification Report** Register this form needs to be completed and provided to the **DNO**, to confirm that the **Micro-generator** has been tested to satisfy the requirements of this EREC G98.

Manufacturer's reference number			MOD 9000TL3-XH.				
Micro-generator technology		MOD 3000TL3-XH, MOD 4000TL3-XH, MOD 5000TL3-XH , MOD 6000TL3-XH, MOD 7000TL3-XH , MOD 8000TL3-XH , MOD 9000TL3-XH, MOD 10KTL3-XH.					
Manufactur	er name		Shenzhen	Growatt New E	Energy Co., Ltd.		
Address			Demonstra	4-13th Floor, Building A, Sino-German Europe Industrial Demonstration Park, No. 1, Hangcheng Avenue, Bao'an District, Shenzhen, Guangdong, China.			
Tel	+86 755 295	51 5888		Fax	+86 755 2747 2131		
E-mail	Peng.zhu@	growatt.com		Web site	www.ginverter.com		
		Connection (Option				
Registered use separate		N/A	kW single phase, single, split or three phase system				
more than of connection of		3-10	kW three phase				
N/A			kW two phases in three phase system				
		N/A	kW two pha	ases split phase	system		

Manufacturer Type Test declaration. - I certify that all products supplied by the company with the above **Type Tested** reference number will be manufactured and tested to ensure that they perform as stated in this document, prior to shipment to site and that no site modifications are required to ensure that the product meets all the requirements of EREC G98.

Signed	On behalf of	Shenzhen Growatt New Energy
	Jeng Thu	Co., Ltd.

Note that testing can be done by the **Manufacturer** of an individual component or by an external test house.

Where parts of the testing are carried out by persons or organisations other than the **Manufacturer** then that person or organisation shall keep copies of all test records and results supplied to them to verify that



the testing has been carried out by people with sufficient technical competency to carry out the tests.

1.Operating Range: This test should be carried out as specified in EN 50438 D.3.1.

Active Power shall be recorded every second. The tests will verify that the **Micro-generator** can operate within the required ranges for the specified period of time.

The Interface Protection shall be disabled during the tests.

In case of a PV Micro-generator the PV primary source may be replaced by a DC source.

In case of a full converter **Micro-generator** (eg wind) the primary source and the prime mover **Inverter**/rectifier may be replaced by a **DC** source.

In case of a DFIG **Micro-generator** the mechanical drive system may be replaced by a test bench motor.

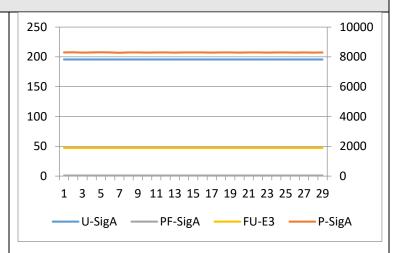
Test 1

Voltage = 85% of nominal (195.5 V),

Frequency = 47 Hz,

Power Factor = 1,

Period of test 20 s.



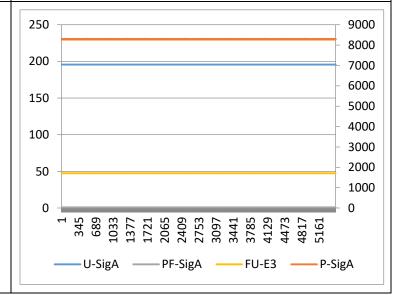
Test 2

Voltage = 85% of nominal (195.5 V)

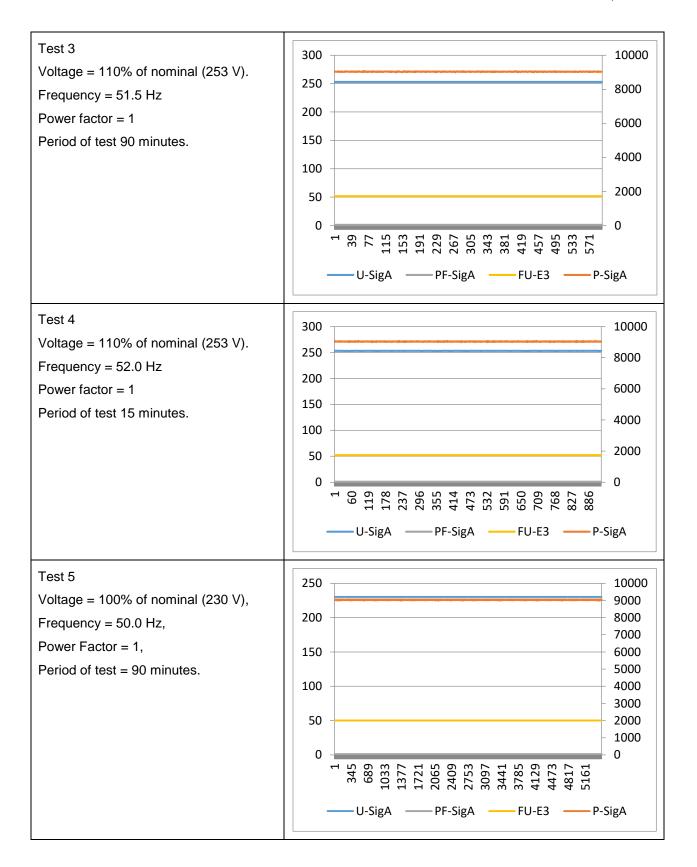
Frequency = 47.5 Hz

Power factor = 1

Period of test 90 minutes.



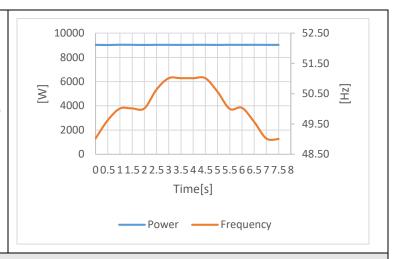






Test 6 RoCoF withstand

Confirm that the Power Generating Module is capable of staying connected to the Distribution Network and operate at rates of change of frequency up to 1 Hzs-1 as measured over a period of 500 ms..



2.Power Quality – Harmonics: These tests should be carried out as specified in BS EN 61000-3-2. The chosen test should be undertaken with a fixed source of energy at two power levels a) between 45 and 55% and b) at 100% of Registered Capacity. The test requirements are specified in Annex A1 A.1.3.1 (Inverter connected) or Annex A2 A.2.3.1 (Synchronous).

Micro-generator tested to BS EN 61000-3-2

Micro-g	Micro-generator rating per phase (rpp)		kW	NV=MV*3.68/rpp
Harmonic	Harmonic At 45-55% of Registered Capacity		Registered	

	Average harmonic current results – Phase 1									
	Value MV in ed value V		Measured Value MV in Amps	alue MV in ed value		Higher limit for odd harmonics 21 and above				
2	0.213	0.235	0.278	0.307	1.080					
3	0.023	0.025	0.039	0.043	2.300					
4	0.136	0.150	0.250	0.276	0.430					
5	0.038	0.042	0.125	0.138	1.140					
6	0.004	0.005	0.002	0.002	0.300					
7	0.042	0.046	0.063	0.070	0.770					
8	0.030	0.033	0.036	0.040	0.230					
9	0.014	0.016	0.008	0.008	0.400					



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10 0.0	036	0.040	0.046	0.051	0.184	
11 0.0	031	0.034	0.029	0.032	0.330	
12 0.0	005	0.005	0.005	0.005	0.153	
13 0.0	039	0.043	0.053	0.058	0.210	
14 0.0	035	0.038	0.055	0.060	0.131	
15 0.0	010	0.011	0.005	0.005	0.150	
16 0.0	021	0.023	0.052	0.057	0.115	
17 0.0	029	0.032	0.052	0.057	0.132	
18 0.0	800	0.009	0.003	0.003	0.102	
19 0.0	017	0.019	0.037	0.041	0.118	
20 0.0	015	0.017	0.033	0.037	0.092	
21 0.0	004	0.005	0.003	0.003	0.107	0.160
22 0.0	018	0.020	0.039	0.043	0.084	
23 0.0	023	0.025	0.028	0.031	0.098	0.147
24 0.0	003	0.004	0.005	0.005	0.077	
25 0.0	020	0.022	0.023	0.025	0.090	0.135
26 0.0	017	0.018	0.020	0.022	0.071	
27 0.0	001	0.001	0.002	0.002	0.083	0.124
28 0.0	011	0.012	0.013	0.014	0.066	
29 0.0	024	0.026	0.024	0.026	0.078	0.117
30 0.0	006	0.007	0.002	0.002	0.061	
31 0.0	014	0.016	0.027	0.030	0.073	0.109
32 0.0	007	0.008	0.015	0.016	0.058	
33 0.0	006	0.006	0.005	0.005	0.068	0.102
34 0.0	004	0.004	0.007	0.008	0.054	
35 0.0	016	0.018	0.021	0.024	0.064	0.096
36 0.0						



37	0.009	0.010	0.019	0.021	0.061	0.091
38	0.006	0.007	0.021	0.023	0.048	
39	0.003	0.003	0.005	0.006	0.058	0.087
40	0.003	0.003	0.006	0.006	0.046	

	Average harmonic current results – Phase 2										
	Measured Value MV in Amps	Normalis ed Value (NV) in Amps	Measured Value MV in Amps	Normalis ed Value (NV) in Amps	Limit in BS EN 61000- 3-2 in Amps	Higher limit for odd harmonics 21 and above					
2	0.171	0.189	0.320	0.353	1.080						
3	0.018	0.020	0.036	0.040	2.300						
4	0.162	0.179	0.256	0.283	0.430						
5	0.018	0.020	0.124	0.137	1.140						
6	0.013	0.015	0.032	0.035	0.300						
7	0.054	0.059	0.080	0.089	0.770						
8	0.030	0.033	0.022	0.024	0.230						
9	0.012	0.013	0.011	0.012	0.400						
10	0.042	0.046	0.044	0.049	0.184						
11	0.030	0.034	0.028	0.031	0.330						
12	0.010	0.011	0.012	0.014	0.153						
13	0.046	0.051	0.051	0.057	0.210						
14	0.033	0.037	0.063	0.070	0.131						
15	0.004	0.004	0.004	0.004	0.150						
16	0.026	0.029	0.051	0.056	0.115						
17	0.029	0.032	0.050	0.055	0.132						
18	0.009	0.010	0.003	0.004	0.102						
19	0.021	0.023	0.033	0.037	0.118						



20	0.006	0.007	0.036	0.040	0.092	
21	0.004	0.004	0.003	0.004	0.107	0.160
22	0.030	0.033	0.027	0.029	0.084	
23	0.022	0.024	0.032	0.035	0.098	0.147
24	0.008	0.009	0.007	0.008	0.077	
25	0.017	0.019	0.020	0.022	0.090	0.135
26	0.018	0.020	0.026	0.029	0.071	
27	0.006	0.007	0.006	0.007	0.083	0.124
28	0.014	0.016	0.015	0.017	0.066	
29	0.021	0.024	0.023	0.026	0.078	0.117
30	0.002	0.002	0.002	0.003	0.061	
31	0.014	0.015	0.023	0.025	0.073	0.109
32	0.008	0.009	0.020	0.022	0.058	
33	0.008	0.009	0.002	0.003	0.068	0.102
34	0.005	0.005	0.007	0.007	0.054	
35	0.015	0.016	0.021	0.023	0.064	0.096
36	0.003	0.004	0.003	0.003	0.051	
37	0.012	0.013	0.020	0.022	0.061	0.091
38	0.002	0.002	0.007	0.008	0.048	
39	0.003	0.003	0.005	0.005	0.058	0.087
40	0.001	0.001	0.013	0.015	0.046	

	Average harmonic current results – Phase 3										
	Measured Value MV in Amps	Normalis ed Value (NV) in Amps	Measured Value MV in Amps	Normalis ed Value (NV) in Amps	Limit in BS EN 61000- 3-2 in Amps	Higher limit for odd harmonics 21 and above					
2	0.103	0.114	0.376	0.416	1.080						



3 0.026 0.028 0.043 0.048 2.300 4 0.156 0.172 0.280 0.309 0.430	
4 0 156 0 172 0 280 0 309 0 430	
0.100 0.172 0.200 0.000 0.400	
5 0.020 0.022 0.098 0.108 1.140	
6 0.020 0.022 0.029 0.032 0.300	
7 0.050 0.055 0.074 0.082 0.770	
8 0.020 0.022 0.031 0.034 0.230	
9 0.012 0.013 0.014 0.400	
10 0.039 0.043 0.048 0.054 0.184	
11 0.040 0.044 0.036 0.040 0.330	
12 0.004 0.005 0.007 0.008 0.153	
13 0.042 0.047 0.056 0.062 0.210	
14 0.026 0.029 0.047 0.052 0.131	
15 0.007 0.008 0.002 0.002 0.150	
16 0.012 0.013 0.053 0.058 0.115	
17 0.033 0.036 0.058 0.064 0.132	
18 0.006 0.006 0.010 0.011 0.102	
19 0.016 0.017 0.040 0.044 0.118	
20 0.013 0.014 0.032 0.036 0.092	
21 0.013 0.014 0.006 0.006 0.107 0.160	
22 0.011 0.012 0.029 0.032 0.084	
23 0.028 0.031 0.036 0.040 0.098 0.147	
24 0.010 0.011 0.007 0.008 0.077	
25 0.014 0.016 0.018 0.020 0.090 0.135	
26 0.021 0.023 0.022 0.024 0.071	
27 0.005 0.006 0.002 0.003 0.083 0.124	
28 0.013 0.014 0.017 0.019 0.066	
29 0.025 0.028 0.029 0.032 0.078 0.117	



30	0.003	0.003	0.008	0.009	0.061	
31	0.008	0.009	0.030	0.033	0.073	0.109
32	0.006	0.007	0.004	0.004	0.058	
33	0.006	0.007	0.009	0.010	0.068	0.102
34	0.010	0.011	0.006	0.006	0.054	
35	0.012	0.013	0.022	0.025	0.064	0.096
36	0.005	0.006	0.018	0.020	0.051	
37	0.008	0.009	0.016	0.018	0.061	0.091
38	0.003	0.004	0.013	0.015	0.048	
39	0.001	0.001	0.003	0.004	0.058	0.087
40	0.002	0.003	0.016	0.018	0.046	

Note the higher limits for odd harmonics 21 and above are only allowable under certain conditions, if these higher limits are utilised please state the exemption used as detailed in part 6.2.3.4 of BS EN 61000-3-2 in the box below.

3.Power Quality – Voltage fluctuations and Flicker: These tests should be undertaken in accordance with EREC G98 Annex A1 A.1.3.3 (Inverter connected) or Annex A2 A.2.3.3 (Synchronous).

	Starting			Stopping			Running	
	d max	d c	d(t)	d max	d c	d(t)	P _{st}	P _{lt} 2 hours
Measured Values at test impedance	0.67	0.29	0	0.63	0.25	0	0.26	0.25
Normalised to standard impedance	0.67	0.29	0	0.63	0.25	0	0.26	0.25
Normalised to required maximum impedance								
Limits set	4%	3.3%	3.3%	4%	3.3%	3.3%	1.0	0.65



under BS EN 61000- 3-11							
Test Impedance	R	0.24	Ω	Х	0.15	Ω	
Standard Impedance	R	0.24 * 0.4 ^	Ω	Х	0.15 * 0.25 ^	Ω	
Maximum Impedance	R	-	Ω	Х	-	Ω	

Applies to three phase and split single phase **Micro-generators**.

^ Applies to single phase **Micro-generators** and **Micro-generators** using two phases on a three phase system.

For voltage change and flicker measurements the following formula is to be used to convert the measured values to the normalised values where the power factor of the generation output is 0.98 or above.

Normalised value = Measured value*reference source resistance/measured source resistance at test point.

Single phase units reference source resistance is 0.4 Ω

Two phase units in a three phase system reference source resistance is $0.4~\Omega$.

Two phase units in a split phase system reference source resistance is $0.24~\Omega$.

Three phase units reference source resistance is 0.24Ω .

Where the power factor of the output is under 0.98 then the X to R ratio of the test impedance should be close to that of the Standard Impedance.

The stopping test should be a trip from full load operation.

The duration of these tests need to conform to the particular requirements set out in the testing notes for the technology under test. Dates and location of the test need to be noted below.

Test start date	13,Jun, 2022	Test end date	13,Jun, 2022
Test location	Growatt certified testin	g laboratory	

4.Power quality – DC injection: This test should be carried out in accordance with EN 50438 Annex D.3.10

Test power level (10K)	20%	50%	75%	100%
Recorded value in Amps	19.4mA/18.6mA/	27.4mA/20.3mA/	19.3mA/19.8mA/	30.1mA/19.8mA/
	25.6mA	18.4mA	28.2mA	21.6mA
as % of rated	0.13%/0.13%/	0.19%/0.14%/	0.13%/0.14%/	0.21%/0.14%/
AC current	0.18%	0.13%	0.19%	0.15%/
Limit	0.25%	0.25%	0.25%	0.25%



Test power level (9K)	20%	50%	75%	100%
Recorded value in Amps	22.4mA/17.5mA/ 18.1mA	24.7mA/19.2mA/ 18.3mA	26.5mA/18.7mA/ 19.5mA	18.8mA/19.7mA/ 27.9mA
as % of rated AC current	0.17%/0.13%/ 0.14%	0.19%/0.15%/ 0.14%	0.20%/0.14%/ 0.15%	0.14%/0.15%/ 0.21%
Limit	0.25%	0.25%	0.25%	0.25%
Test power level (8K)	20%	50%	75%	100%
Recorded value in Amps	15.2mA/16.8mA/ 19.5mA	15.5mA/16.7mA/ 19.8mA	17.2mA/18.3mA/ 21.2mA	23.6mA/18.8mA/ 19.1mA
as % of rated AC current	0.13%/0.14%/ 0.17%	0.13%/0.14%/ 0.17%	0.15%/0.16%/ 0.18%	0.20%/0.16%/ 0.16%
Limit	0.25%	0.25%	0.25%	0.25%
Test power level (7K)	20%	50%	75%	100%
Recorded value in Amps	15.2mA/16.4mA/ 17.5mA	17.4mA/16.6mA/ 15.5mA	18.5mA/15.4mA/ 16.6mA	17.5mA/18.1mA/ 19.3mA
as % of rated AC current	0.15%/0.16%/ 0.17%	0.17%/0.16%/ 0.15%	0.18%/0.15%/ 0.16%	0.17%/0.18%/ 0.19%
Limit	0.25%	0.25%	0.25%	0.25%
Test power level (6K)	20%	50%	75%	100%
Recorded value in Amps	12.2mA/11.5mA/ 13.5mA	11.7mA/12.1mA/ 13.8mA	12.7mA/13.1mA/ 15.2mA	13.2mA/12.9mA/ 15.6mA
as % of rated AC current	0.14%/0.13%/ 0.16%	0.13%/0.14%/ 0.16%	0.15%/0.15%/ 0.17%	0.15%/0.15%/ 0.18%
Limit	0.25%	0.25%	0.25%	0.25%
Test power level (5K)	20%	50%	75%	100%
Recorded value in Amps	10.4mA/11.3mA/ 11.7mA	10.5mA/11.4mA/ 12.1mA	12.5mA/10.8mA/ 11.6mA	11.2mA/12.1mA/ 13.2mA
as % of rated AC current	0.14%/0.16%/ 0.16%	0.15%/0.16%/ 0.17%	0.17%/0.15%/ 0.16%	0.15%/0.17%/ 0.18%
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Limit	0.25%	0.25%	0.25%	0.25%
Test power level (4K)	20%	50%	75%	100%
Recorded value in Amps	8.64mA/8.85mA/ 9.82mA	8.72mA/9.02mA/ 9.72mA	10.4mA/9.53mA/ 8.75mA	11.3mA/9.6mA/ 9.3mA
as % of rated AC current	0.15%/0.15%/ 0.17%	0.15%/0.16%/ 0.17%	0.18%/0.16%/ 0.15%	0.19%/0.17%/ 0.16%
Limit	0.25%	0.25%	0.25%	0.25%
Test power level (3K)	20%	50%	75%	100%
Recorded value in Amps	6.51mA/6.72mA/ 7.06mA	6.63mA/6.98mA/ 7.32mA	6.78mA/7.22mA/ 7.58mA	7.12mA/7.45mA/ 7.89mA
as % of rated AC current	0.15%/0.15%/ 0.16%	0.15%/0.16%/ 0.17%	0.16%/0.17%/ 0.17%	0.16%/0.17%/ 0.18%
Limit	0.25%	0.25%	0.25%	0.25%

5.Power Quality – Power factor: This test shall be carried out in accordance with EN 50548 Annex D.3.4.1 but with nominal voltage -6% and +10%. Voltage to be maintained within $\pm 1.5\%$ of the stated level during the test.

	216.2 V	230 V	253 V
20% of Registered Capacity	0.9924	0.9931	0.9922
50% of Registered Capacity	0.9958	0.9964	0.9957
75% of Registered Capacity	0.9970	0.9975	0.9969
100% of Registered Capacity	0.9988	0.9993	0.9986
Limit	>0.95	>0.95	>0.95

6.Protection – Frequency tests: These tests should be carried out in accordance with EN 50438 Annex D.2.4 and the notes in EREC G98 Annex A1 A.1.2.3 (**Inverter** connected) or Annex A2 A.2.2.3 (Synchronous)

Function	Setting	Trip test	"No trip tests"
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	Frequency	Time delay	Frequency	Time delay	Frequency /time	Confirm no trip
U/F stage 1	47.5 Hz	20 s	47.50Hz	20.019s	47.7 Hz 30 s	No Trip
U/F stage 2	47 Hz	0.5 s	47.00Hz	0.520s	47.2 Hz 19.5 s	No Trip
					46.8 Hz 0.45 s	No Trip
O/F stage	52 Hz	0.5 s	52.01Hz	0.516s	51.8 Hz 120.0 s	No Trip
					52.2 Hz 0.45 s	No Trip

Note. For frequency trip tests the frequency required to trip is the setting \pm 0.1 Hz. In order to measure the time delay a larger deviation than the minimum required to operate the projection can be used. The "No trip tests" need to be carried out at the setting \pm 0.2 Hz and for the relevant times as shown in the table above to ensure that the protection will not trip in error.

7.Protection – Voltage tests: These tests should be carried out in accordance with EN 50438 Annex D.2.3 and the notes in EREC G98 Annex A1 A.1.2.2 (Inverter connected) or Annex A2 A.2.2.2 (Synchronous)

Function	Setting		Trip test		"No trip tests"		
	Voltage	Time delay	Voltage	Time delay	Voltage /time	Confirm no trip	
U/V	184 V	2.5 s	183.6V	2.514 s	188 V 5.0 s	No Trip	
					180 V 2.45 s	No Trip	
O/V stage 1	262.2 V	1.0 s	262.54V	1.021s	258.2 V 5.0 s	No Trip	
O/V stage 2	273.7 V	0.5 s	274.23V	0.517s	269.7 V 0.95 s	No Trip	
					277.7 V 0.45 s	No Trip	

Note for Voltage tests the Voltage required to trip is the setting ± 3.45 V. The time delay can be measured at a larger deviation than the minimum required to operate the protection. The No trip tests need to be carried out at the setting ± 4 V and for the relevant times as shown in the table above to ensure that the protection will not trip in error.

8.Protection – Loss of Mains test: For PV Inverters shall be tested in accordance with BS EN 62116. Other Inverters should be tested in accordance with EN 50438 Annex D.2.5 at 10%, 55% and 100% of rated power.

To be carried out at three output power levels with a tolerance of plus or minus 5% in Test Power levels.



Test Power	10%	55%	100%	10%	55%	100%
Balancing load on islanded network	95% of Registered Capacity	95% of Registered Capacity	95% of Registered Capacity	105% of Registered Capacity	105% of Registered Capacity	105% of Registered Capacity
Trip time. Limit is 0.5 s	0.312s	0.374s	0.335s	0.278s	0.335 s	0.385 s
For Multi phase M single fuse as well			t the device s	huts down coi	rrectly after the	e removal of a
Test Power	10%	55%	100%	10%	55%	100%
Balancing load on islanded network	95% of Registered Capacity	95% of Registered Capacity	95% of Registered Capacity	105% of Registered Capacity	105% of Registered Capacity	105% of Registered Capacity
Trip time. Ph1 fuse removed	0.285 s	0.321 s	0.348 s	0.312 s	0.323 s	0.354 s
Test Power	10%	55%	100%	10%	55%	100%
Balancing load on islanded network	95% of Registered Capacity	95% of Registered Capacity	95% of Registered Capacity	105% of Registered Capacity	105% of Registered Capacity	105% of Registered Capacity
Trip time. Ph2 fuse removed	0.318 s	0.367 s	0.349 s	0.332 s	0.349 s	0.352 s
Test Power	10%	55%	100%	10%	55%	100%
Balancing load on islanded network	95% of Registered Capacity	95% of Registered Capacity	95% of Registered Capacity	105% of Registered Capacity	105% of Registered Capacity	105% of Registered Capacity
Trip time. Ph3 fuse removed	0.353 s	0.366 s	0.352 s	0.354 s	0.309 s	0.314 s
Note for technologestablishing that the 1.0 s for these tech	ne trip occurred					
Indicate additional	shut down time	e included in a	bove results.		40ms	
For Inverters teste table.	ed to BS EN 6	2116 the follo	wing sub set o	of tests should	be recorded in	n the following
Test Power and	33%	66%	100%	33%	66%	100%
imbalance	-5% Q	-5% Q	-5% P	+5% Q	+5% Q	+5% P
	Test 22	Test 12	Test 5	Test 31	Test 21	Test 10
Trip time. Limit	0.302	0.359	0.378	0.295	0.295 0.356	



is 0.5 s								
9.Protection – Frequence accordance with EREC G9								
	Start Frequency	Chang	Change Confir			irm n	o trip	
Positive Vector Shift	49.0 Hz	+50 de	egrees		No T	rip		
Negative Vector Shift	50.0 Hz	- 50 de	egrees		No T	rip		
10.Protection - Frequen 11.3, test procedure in Ann								
Ramp range	Test frequency rar	mp:	Test D	urati	on	Con	firm no trip	
49.0 Hz to 51.0 Hz	+0.95 Hzs ⁻¹		2.1 s			No	Ггір	
51.0 Hz to 49.0 Hz	-0.95 Hzs ⁻¹		2.1 s			No ⁻	Ггір	
11.Limited Frequency S accordance with EN 50438 out using the specific thres	3 Annex D.3.3 Powe	r respo	onse to o	ver-	frequ	iency		
Test sequence at Registered Capacity >80%	Measured Active Power Output	Freq	uency	Pri	Primary Power Source			Active Power Gradient
Step a) 50.00 Hz ±0.01 Hz	9022.45W	50.0	1Hz	92	43.6	5W		-
Step b) 50.45 Hz ±0.05 Hz	8930.25W	50.4	6Hz					-
Step c) 50.70 Hz ±0.10 Hz	8482.65W	50.7	'0Hz					-
Step d) 51.15 Hz ±0.05 Hz	7728.34W	51.1	4Hz					-
Step e) 50.70 Hz ±0.10 Hz	8475.45W	50.7	'1Hz					-
Step f) 50.45 Hz ±0.05 Hz	8933.19W	50.4	4Hz					-
Step g) 50.00 Hz ±0.01 Hz	9024.56W	50.0	0Hz					
Test sequence at Registered Capacity 40% - 60%	Measured Active Power Output		uency	P		Active Power Gradient		
Step a) 50.00 Hz ±0.01 Hz	4498.67W	49.9	9Hz	46	15.3	5W		-
Step b) 50.45 Hz ±0.05 Hz	4410.25W	50.4	6Hz					-
Step c) 50.70 Hz ±0.10 Hz	3955.43W	50.7	'0Hz					-
Step d) 51.15 Hz ±0.05 Hz	3160.24W	51.1	5Hz					-



Step e) 50.7	70 Hz ±0.10 Hz	39	61.02W		50.71	Hz				-
Step f) 50.4	5 Hz ±0.05 Hz	44	11.59W		50.45	Hz				-
Step g) 50.0	00 Hz ±0.01 Hz	45	03.57W		50.00	Hz				
Steps as de	efined in EN 504	38				·				·
	output with fall ex D.3.2 active p						d be ca	arried	out in acc	cordance with EN
Test sequence			Measured Power Ou		Active t	Frequency		Primary	power source	
Test a) 50 H	Hz ± 0.01 Hz		9023.42 V	V		50.00 Hz	<u>z</u>		9230.45	W
Test b) Poi and 49.6 Hz	nt between 49.5	5 Hz	9018.76W	/		49.50 Hz	Z		9225.62	W
Test c) Poi and 47.6 Hz	nt between 47.5	5 Hz	9011.28W			47.51 Hz			9216.79 W	
NOTE: The	operating point	in Tes	st (b) and (d	c) sh	nall be n	naintained	for at	least :	5 minutes	
13.Re-conr	nection timer.									
	prove that the r						minimu	ım del	ay of 20 s	for restoration of
Time delay setting	Measured delay					nnection v limits of t			or freque	ency is brought to
20s	30.0S		At 266.2	2 V	At 180).0 V	At 4	7.4 Hz	_	At 52.1 Hz
	n that the No		- Yes		Yes		Yes	1		Yes
	vel contribution verter connected							dance	with ERE	C G98 Annex A1
,	es with electro-n					For Inve	,	utput		
Parameter Symbol Value Time after Volts Amps						Amps				
Peak Short Circuit current i _p						20 ms		97.2\	/	25.8A
Initial Value	of aperiodic cur	rent	Α			100 ms		41.5\	/	14.6
Initial symm	netrical short-circ	uit	I _k			250 ms		60.3\	/	15.8



Decaying (aperiodic) component of short circuit current*	i _{DC}	 500 ms	45.6V	9.9
Reactance/Resistance Ratio of source*	X/ _R	 Time to trip	0.229	In seconds

For rotating machines and linear piston machines the test should produce a 0 s - 2 s plot of the short circuit current as seen at the **Micro-generator** terminals.

* Values for these parameters should be provided where the short circuit duration is sufficiently long to enable interpolation of the plot

This equipment is equipped with R.145 terminal for logic interface that	Yes	
This equipment is equipped with RJ45 terminal for logic interface that being received the signal from the DNO, the connection should be installed per installation manual, and the signal should be a simple binary output that captured by RJ45 terminal(PIN 5 and 1 for detecting the signal). Once the signal actived, the inverter will reduce its active power to zero within 5s.		
16.Self-Monitoring solid state switching: No specified test requirements. Refer to EREC G98 Annex A1 A.1.3.6 (Inverter connected).	Yes/or NA	
It has been verified that in the event of the solid state switching device failing to disconnect the Micro-generator , the voltage on the output side of the switching device is reduced to a value below 50 V within 0.5 s.		
17. Cyber security	Yes/or NA	
Confirm that the Manufacturer or Installer of the Micro-generator has provided a statement describing how the Micro-generator has been designed to comply with cyber security requirements, as detailed in 9.7.	Yes	
Additional comments		

Manufacturer's declaration in accordance with the requirements of G98-Amd. 6 (2021-09) standard Sec.s 9.7.1, 9.7.2, and G99-Amd. 8 (2021-09) standard Sec.s 9.1.7, 9.1.8 regarding "Cyber Security"

The undersigned *****,





as Marketing department of the Company ShenZhen Growatt New Energy Co.,Ltd.,

based in 4-13/F, Building A, Sino- German(Europe) Industrial Park, Hangcheng Ave, Guxing Community, Xixiang Subdistrict, Bao'an District, Shenzhen, China

on behalf of the same Company declares the following:

1) The ShenZhen Growatt New Energy Co.,Ltd company's inverters include a system of internal and

external logic communications as summarized in the following scheme:



where the main components involved and their main functions are explained in the following table:

Name	Meaning	Function	Location
PMS	Power	monitoring and management of	Inverter
	Management	power fluxes through the inverter,	
	System	execution of local logic functions	
		depending on grid parameters	



		values	
Monitoring	WIFI/GPRS	Monitoring device to realize remote monitoring function	Monitoring device
Router	Router device	transmission of data to cloud server, reception of commands/settings from external stakeholder	Third-party device
Meter	External Power Meter	meter at the AC input site, and possible meter at AC port of third party generator/inverter, for power measures	Third-party device

and the subjects/parties involved in communications with the Growatt inverters are listed in the

following table, together with the purposes of the respective communications:

Subject	Meaning	Operations
End-user	mobile device (App), PC (web portal)	monitoring of historical data, settings for special functions
Service	PC (via web portal)	remote diagnosis, system behaviour monitoring, remote updates, remote settings

- 2) All communications between internal components of the inverter, and supplied External Power Meter(s), take place via appropriate serial lines (RS485, CanBus).
- 3) The only communication port between the inverter and the outside is constituted by the monitoring device on the system; the communication between inverter and the outside world can take place via an Ethernet line, WiFi or GPRS router according to the customer's request.
- 4) All communications between the Growatt server and the subjects/parties are cyber-protected by SSL

technology.

5) The cyber-security assessment of the Growatt was performed according to the ETSI EN 303 645



standard, and it is reported according to the Table B.1 form of the same standard:

EN 303 645 v2.1.1 (20	020-06) Table B.1: Im	nplementation of p	rovisions for consumer IoT security
Clause number and ti	tle		
Reference	Status	Support	Detail
5.1 No universal defa	ult passwords	-	·
Provision 5.1-1	M C (1)	N/A	
Provision 5.1-2	M C (1)	N/A	
Provision 5.1-3	M	N/A	There is no default passwords fo
Provision 5.1-4	M C (8)	N/A	users
Provision 5.1-5	M C (5)	N/A	
5.2 Implement a mea	ns to manage report	s of vulnerabilities	
Provision 5.2-1	M	Υ	
Provision 5.2-2	R	Υ	
Provision 5.2-3	R	Υ	
5.3 Keep software up	dated		
Provision 5.3-1	R	Υ	
Provision 5.3-2	MC (5)	Υ	
Provision 5.3-3	MC (12)	N/A	
Provision 5.3-4	RC (12)	Y	The manufacturer manages the updates of the systems by means of remote automatic, selectively by type of machine or by activating special functions at the request of the user
Provision 5.3-5	RC (12)	N	Check note at 5.3-4
Provision 5.3-6	RC (9,12)	N	Check note at 5.3-4
Provision 5.3-7	M C (12)	Υ	
Provision 5.3-8	M C (12)	N	note at 5.3-4
Provision 5.3-9	R C (12)	N	
Provision 5.3-10	M (11,12)	Υ	
Provision 5.3-11	RC (12)	N	
Provision 5.3-12	RC (12)	N	
Provision 5.3-13	M	Υ	
Provision 5.3-14	R C (3,4)	N/A	
Provision 5.3-15	R C (3,4)	N/A	
Provision 5.3-16	M	Υ	
5.4 Securely store ser	nsitive security parar	meters	
Provision 5.4-1	M	Υ	
Provision 5.4-2	M(10)	Υ	
Provision 5.4-3	М	N/A	hard-coded identity not used in source code
Provision 5.4-4	M	Υ	
5.5 Communicate sec	urely		
Provision 5.5-1	M	Υ	
Provision 5.5-2	R	Υ	
Provision 5.5-3	R	Υ	
Provision 5.5-4	R	N	
Provision 5.5-5	М	Υ	
Provision 5.5-6	R	Υ	



		T	
Provision 5.5-7	M	Υ	
Provision 5.5-8	M	Υ	
5.6 Minimize exposed	l attack surfaces		
Provision 5.6-1	M	Υ	
Provision 5.6-2	M	Υ	
Provision 5.6-3	R	Υ	
Provision 5.6-4	MC(13)	N/A	
Provision 5.6-5	R	Υ	
Provision 5.6-6	R	Υ	
Provision 5.6-7	R	Υ	
Provision 5.6-8	R	N	
Provision 5.6-9	R	Υ	
5.7 Ensure software in	ntegrity	J	<u>'</u>
Provision 5.7-1	R	N	
Provision 5.7-2	R	N	
5.8 Ensure that perso		T .	
Provision 5.8-1	R	N/A	
Provision 5.8-2	М	Υ	applicable to server/cloud services and
			to the customer App for mobile
			devices
Provision 5.8-3	M	Υ	
5.9 Make systems res			
Provision 5.9-1	R	Υ	
Provision 5.9-2	R	Υ	
Provision 5.9-3	R	Υ	
5.10 Examine system	telemetry data		
Provision 5.10-1	RC (6)	N	
5.11 Make it easy for	users to delete us	er data	
Provision 5.11-1	M	N/A	
Provision 5.11-2	R	N/A	
Provision 5.11-3	R	N/A	
Provision 5.11-4	R	N/A	
5.12 Make installation	n and maintenance	e of devices easy	-
Provision 5.12-1	R	N/A	no istallation/maintenance operations
			are available to the final user
Provision 5.12-2	R	N/A	no istallation/maintenance operations
		'	are available to the final user
Provision 5.12-3	R	N/A	check note at 5.3-4
5.13 Validate input da			1
Provision 5.13-1	M	Υ	
6 Data protection pro			1
Provision 6.1	M	Υ Υ	it only applies to the server/cloud side
5	1**	•	of the service
Provision 6.2	MC (7)	Υ	it only applies to the server/cloud side
	1410 (7)	'	of the service
Provision 6.3	M	Υ	it only applies to the server/cloud side
	1*1	'	of the service
Provision 6.4	RC (6)	Y	or the service
Provision 6.5	MC(6)	Y	
Conditions:	IVIC(U)		
1) passwords are used			

- 1) passwords are used;
- 2) pre-installed passwords are used;
- 3) software components are not updateable;



- 4) the device is constrained;
- 5) the device is not constrained;
- 6) telemetry data being collected;
- 7) personal data is processed on the basis of consumers' consent;
- 8) the device allowing user authentication;
- 9) the device supports automatic updates and/or update notifications;
- 10) a hard-coded unique per device identity is used for security purposes;
- 11) updates are delivered over a network interface;
- 12) an update mechanism is implemented;
- 13) a debug interface is physically accessible.

Status' Column:

M: Mandatory provision

R: Recommended provision

M C: Mandatory and conditional provision R C: Recommended and conditional provision

Support' Column:

Y: Implemented

N: Not implemented

N/A: Not applicable